

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Farnworth et al.

Serial No.: Not Yet Assigned

Filed: September 19, 2003

For: METHOD AND APPARATUS FOR
SUPPORTING WAFERS FOR DIE
SINGULATION AND SUBSEQUENT
HANDLING

Confirmation No.: Unknown

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 2269-5529US
(02-0766.00/US)

NOTICE OF EXPRESS MAILING

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Person making Deposit: Chris Haughton

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in

37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56(b) exists.

<u>U.S. Patent Documents</u>		
<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
3,976,288	08/24/1976	Cuomo, Jr.
4,925,515	05/15/1990	Yoshimura et al.
5,460,703	10/24/1995	Nulman et al.
5,705,016	01/06/1998	Senoo et al.
6,039,830	03/21/2000	Park et al.
6,150,240	11/21/2000	Lee et al.
6,245,646 B1	06/12/2001	Roberts
6,344,402 B1	02/05/2002	Sekiya
6,498,074 B2	12/24/2002	Siniaguine et al.

Other Documents

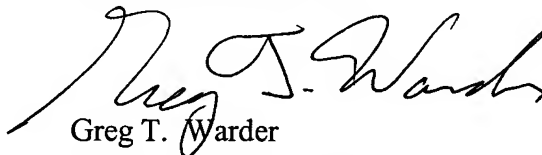
U.S. Patent Application Serial No. 10/094,161, filed March 6, 2002, for “Encapsulated Semiconductor Components and Methods of Fabrication.”

Applicants offer to supply any explanation or discussion of the documents that the Examiner feels is necessary or desirable and which is requested.

Attorney Docket No.: 2269-5529US

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. § 1.17(p) is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Greg T. Warder", is written over the typed name.

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Enclosures: Form PTO-1449
Cited Documents
Document in ProLaw

Form PTO-1449

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

(Use several sheets if necessary)

Docket Number (Optional)
5529US (02-0766.00/US)

Application Number
N t Yet Assign d

Applicant **Farnworth t al.**Filing Date **S ptember 19, 2003**Group Art Unit **Unknown**

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,976,288	08/24/1976	Cuomo, Jr.			
	4,925,515	05/15/1990	Yoshimura et al.			
	5,460,703	10/24/1995	Nulman et al.			
	5,705,016	01/06/1998	Senoo et al.			
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	6,245,646 B1	06/12/2001	Roberts			
	6,344,402 B1	02/05/2002	Sekiya			
	6,498,074 B2	12/24/2002	Siniaguine et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

U.S. Patent Application Serial No. 10/094,161, filed March 6, 2002, for "Encapsulated Semiconductor Components and Methods of Fabrication."

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not consid red. Include copy of this form with n xt communication to th applicant.